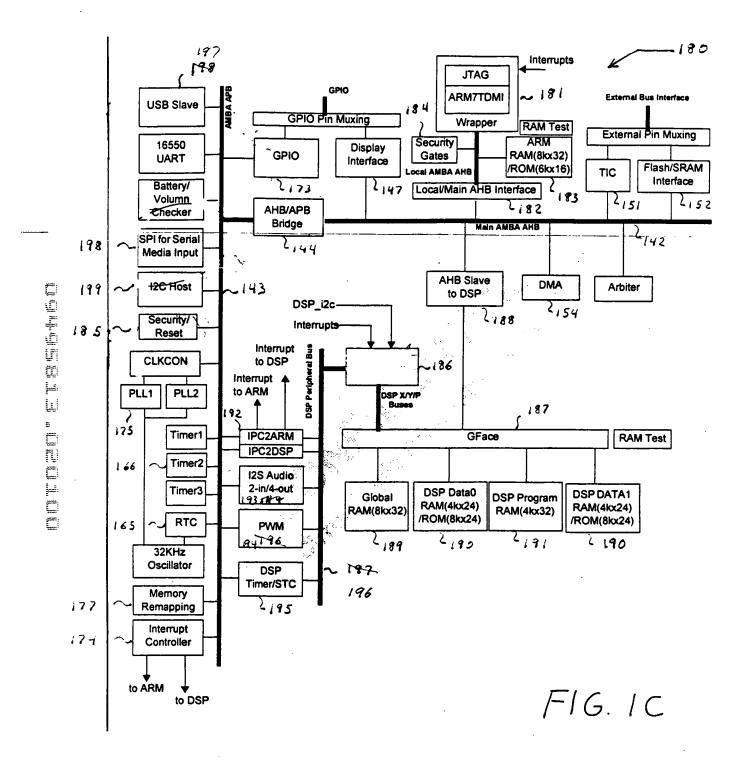
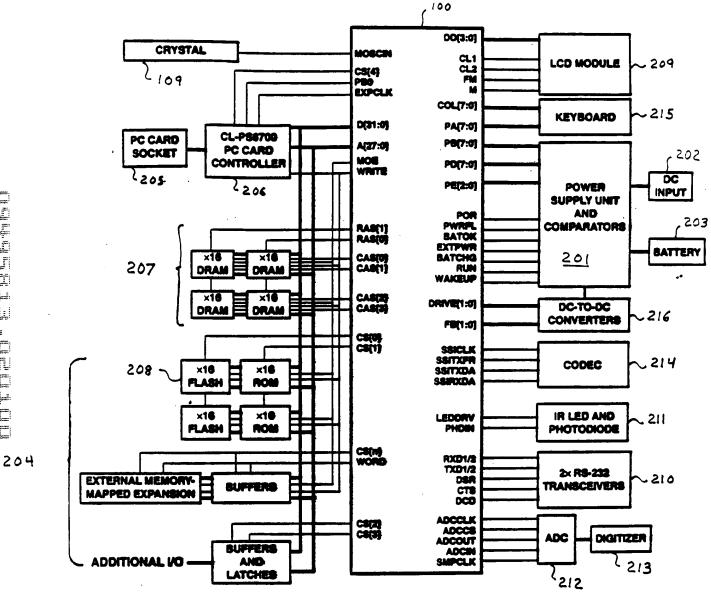
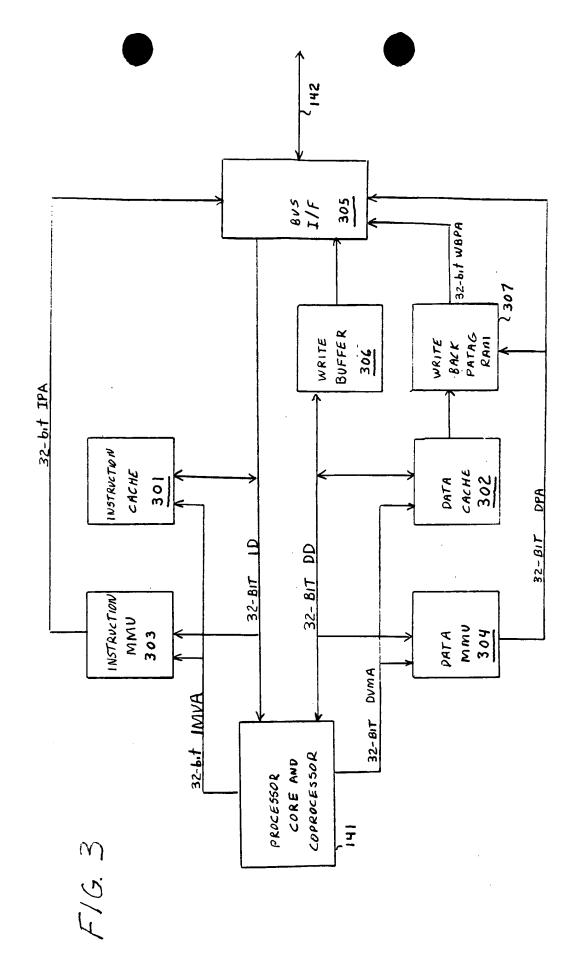


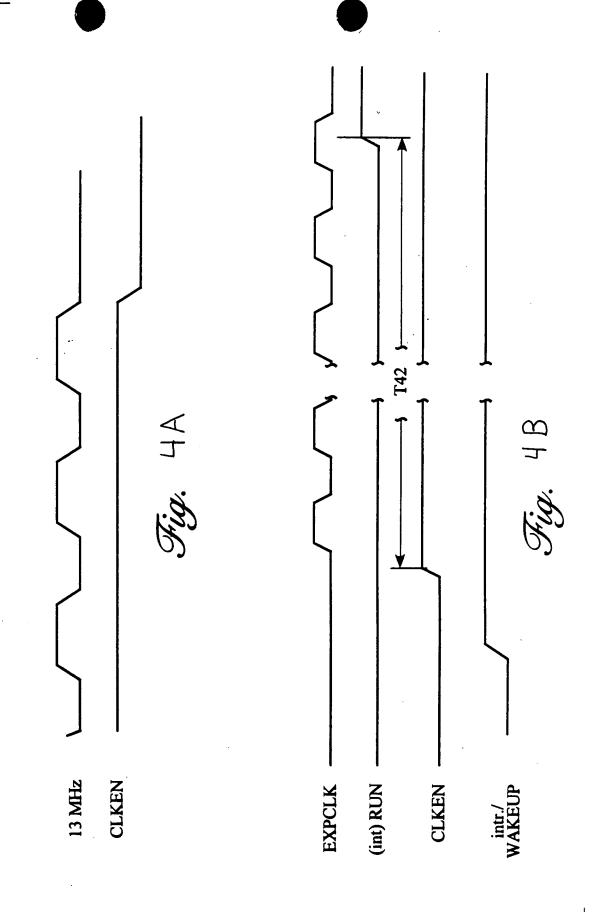
Fig. 1B



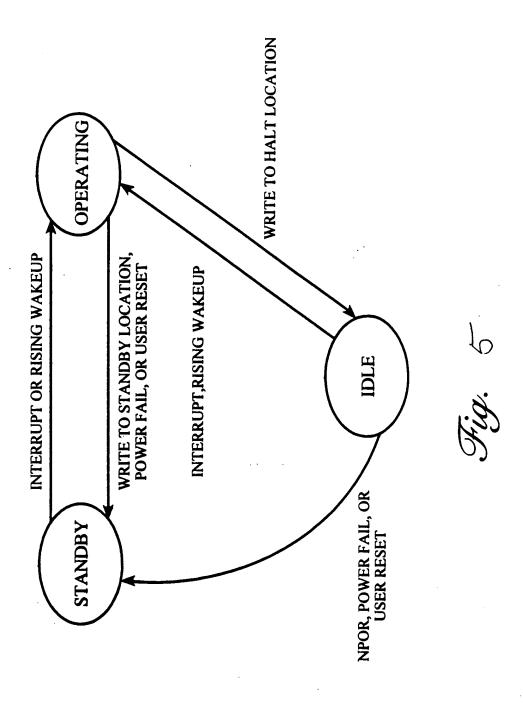


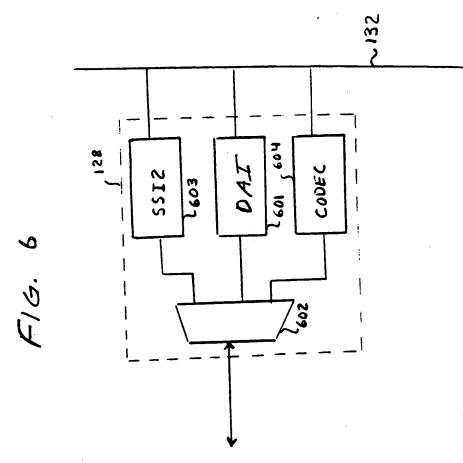
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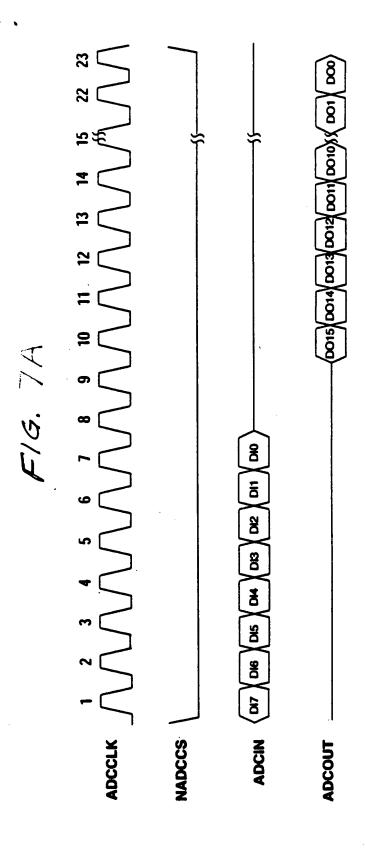




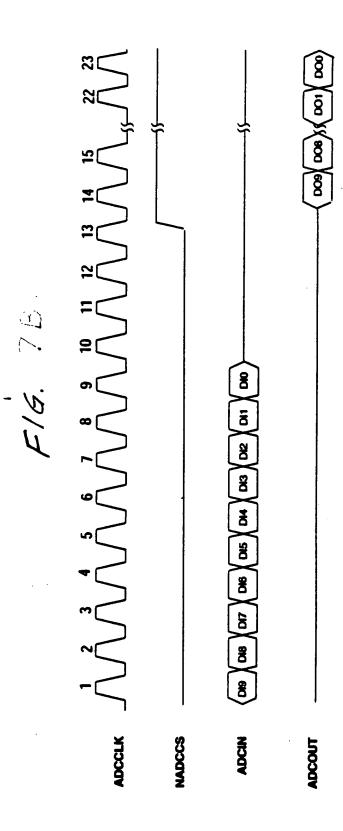
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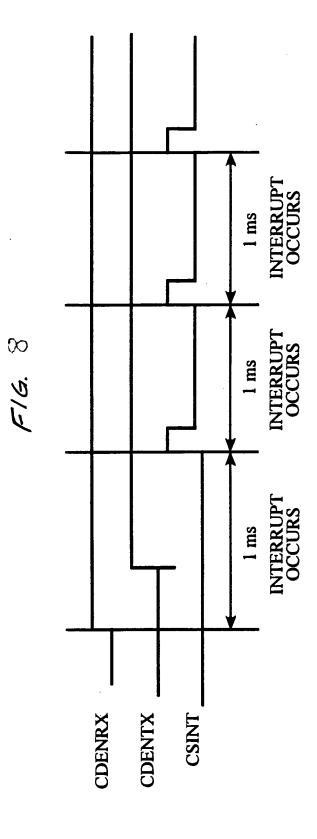




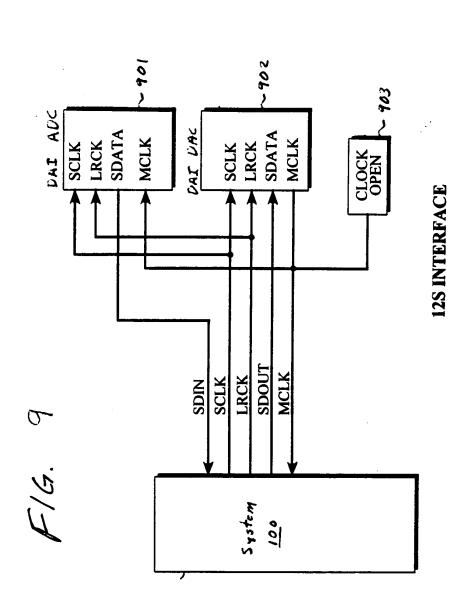
SSI Timing Interface for MAX148/9



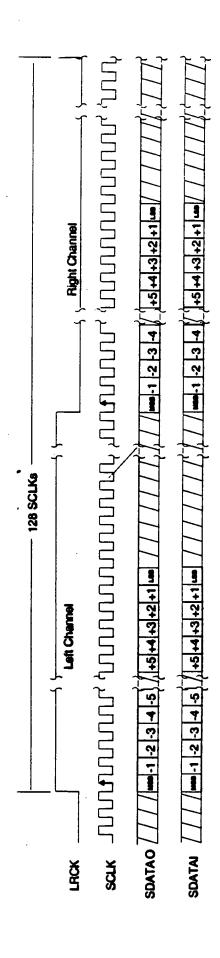
SSI Interface for AD7811/2

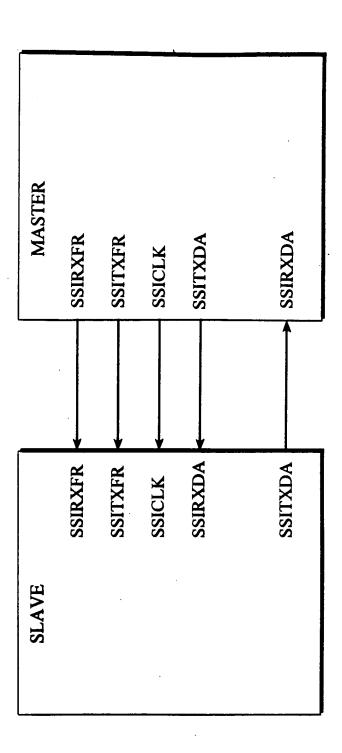


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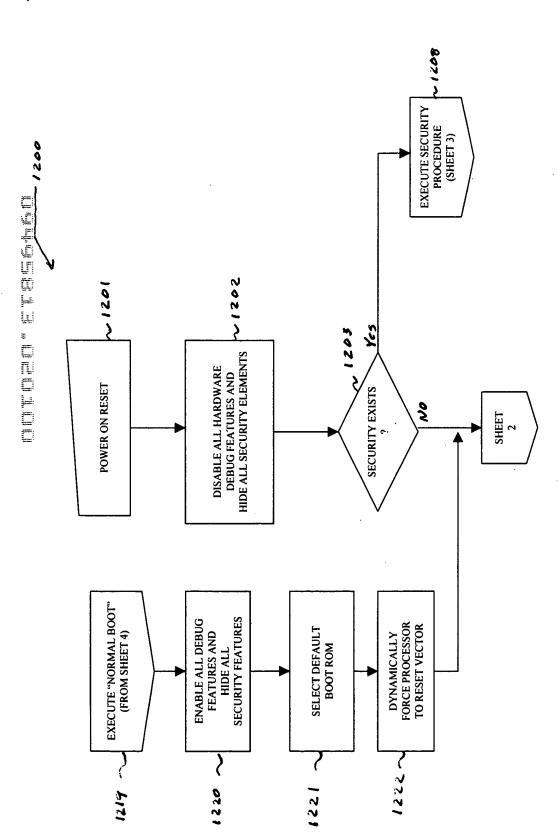


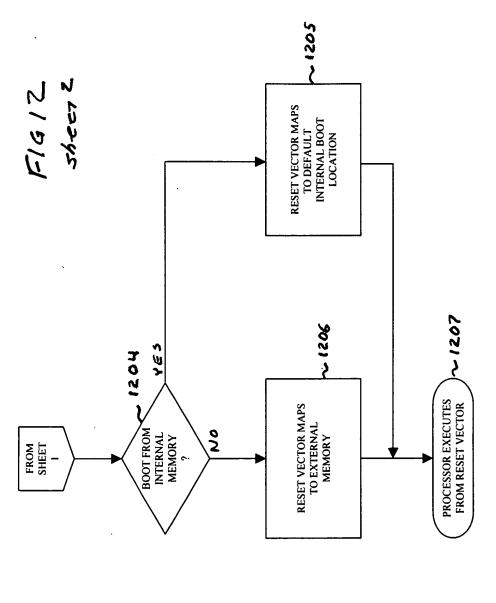
F16. 10

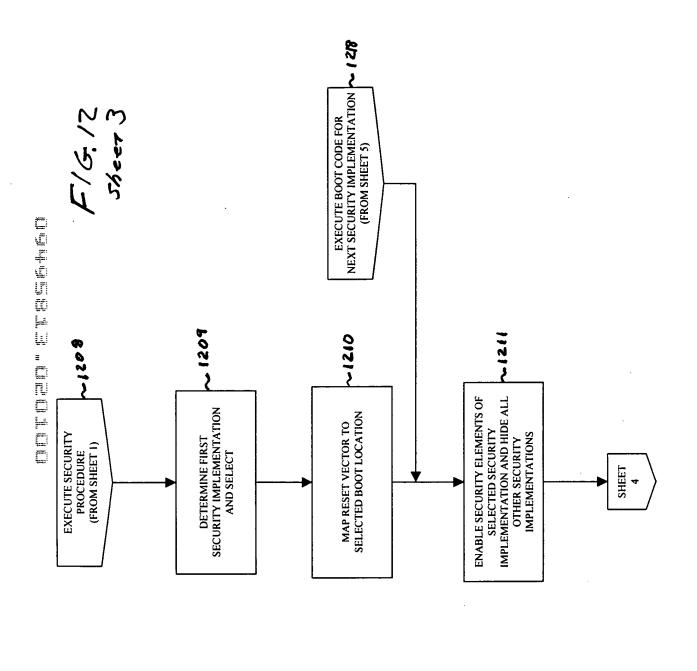


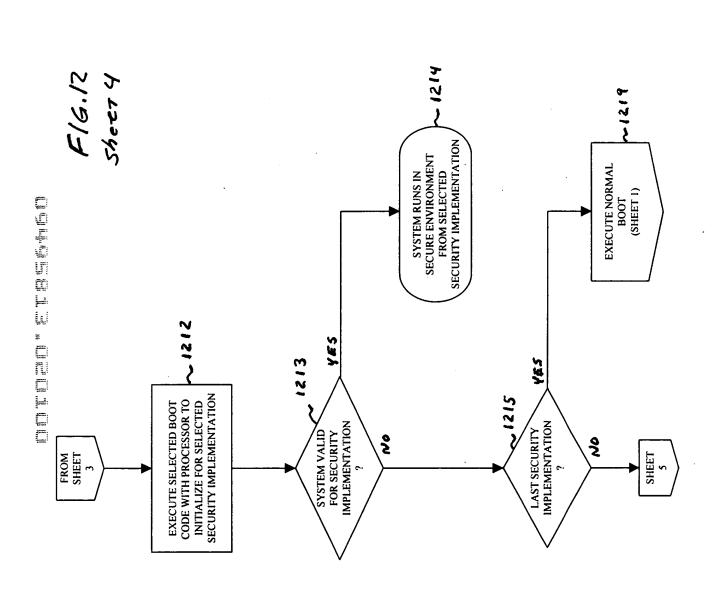


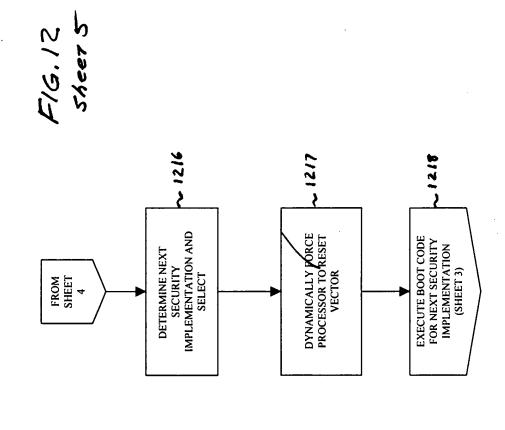
F16.11

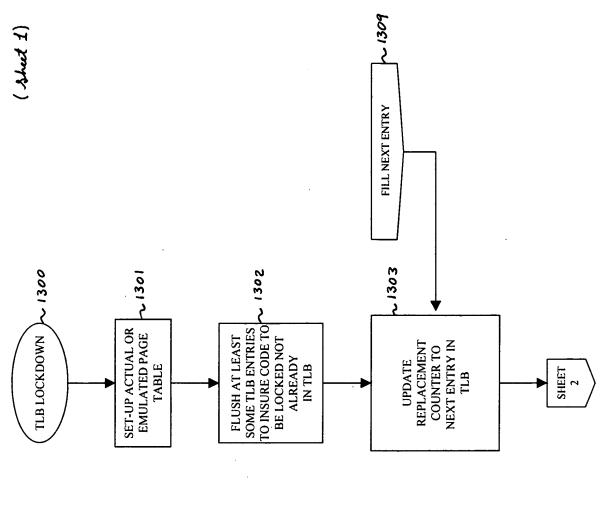


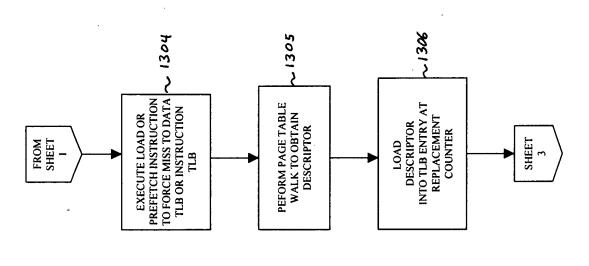


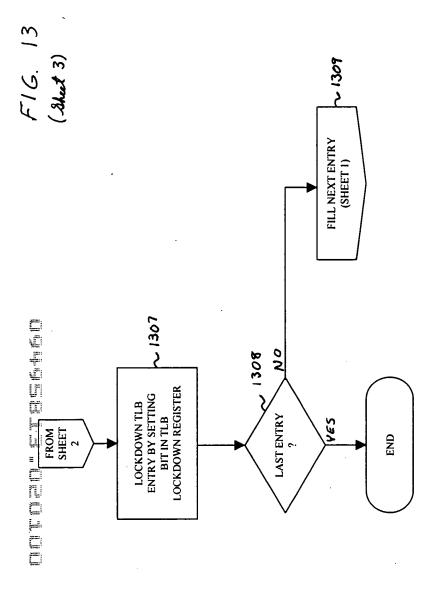












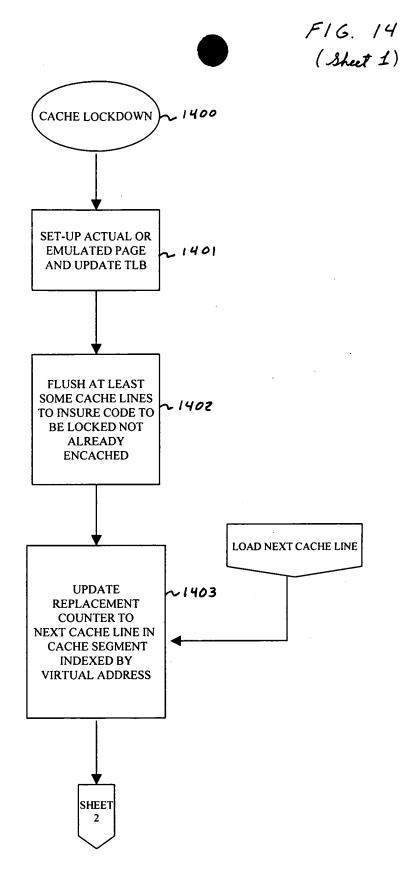
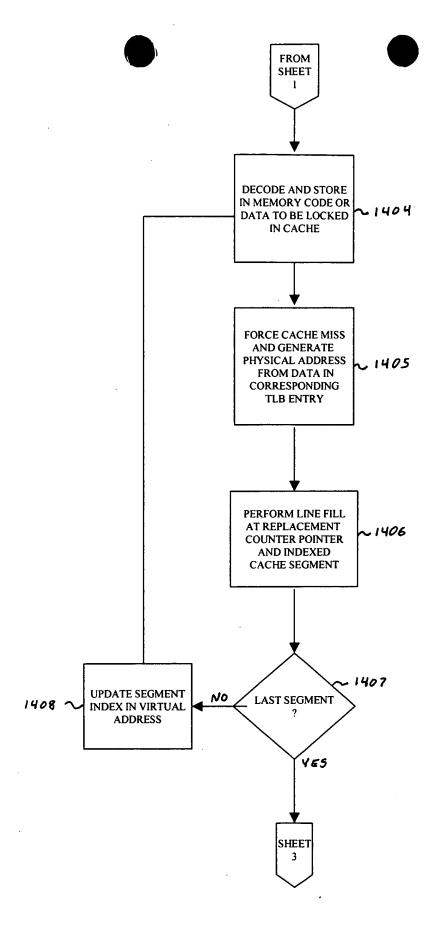


FIG. 14 (Sheet 2)



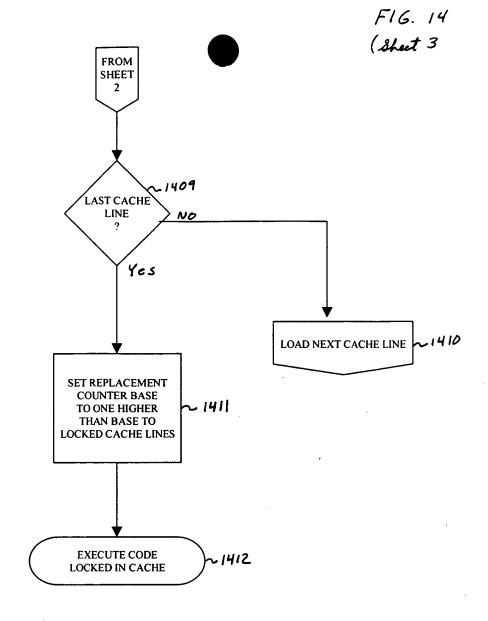
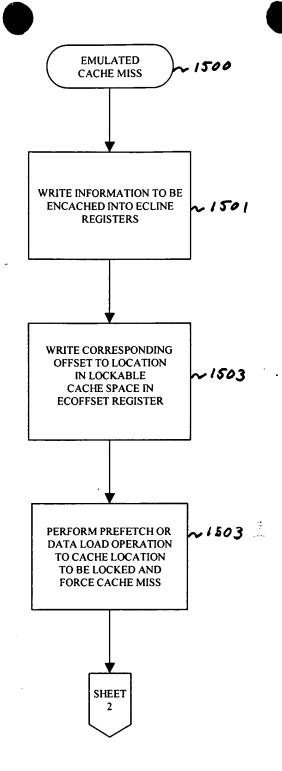
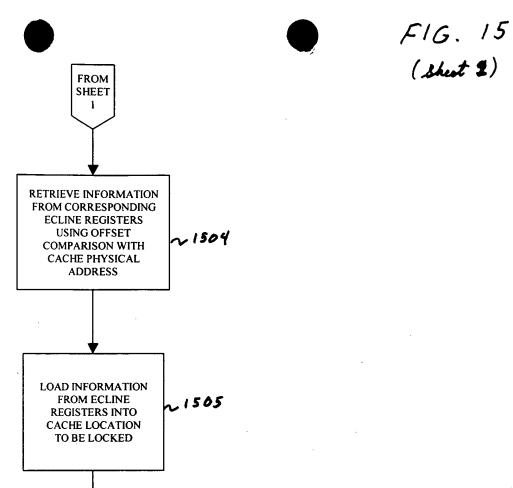


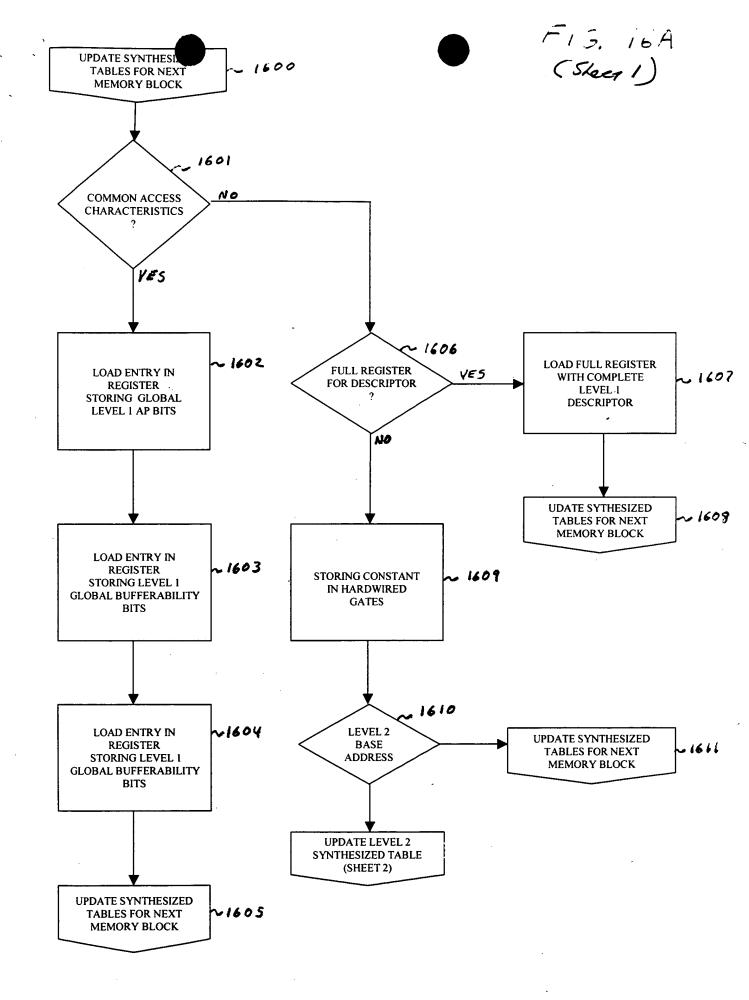
FIG. 15 (Sheet 1)

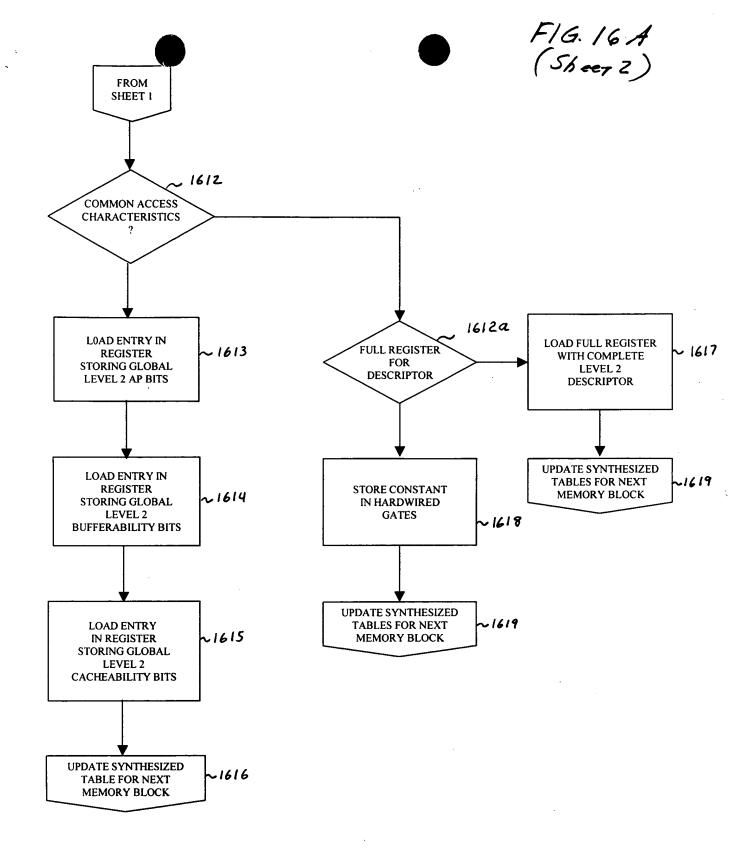


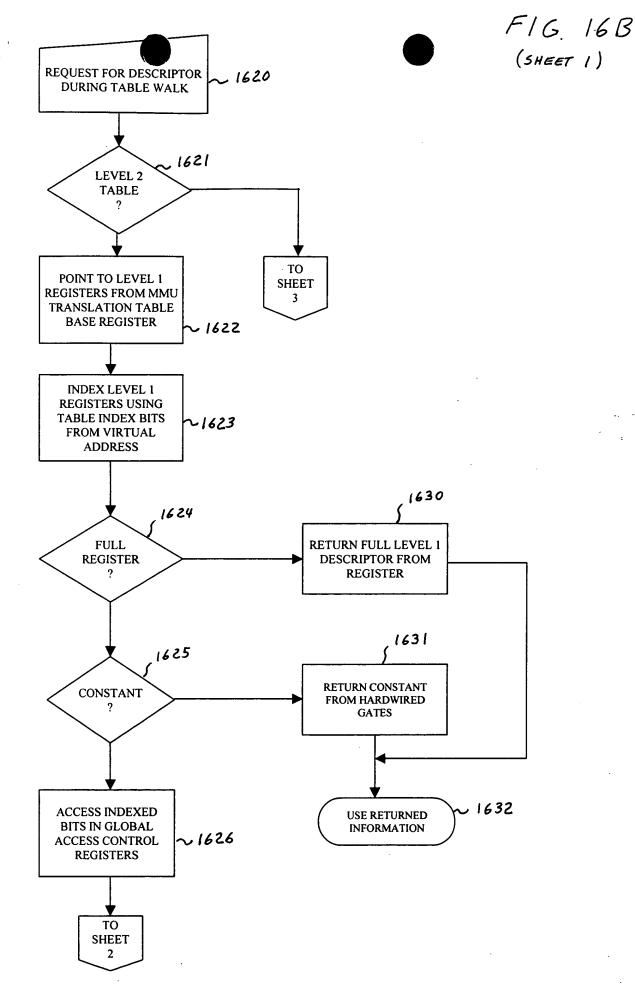
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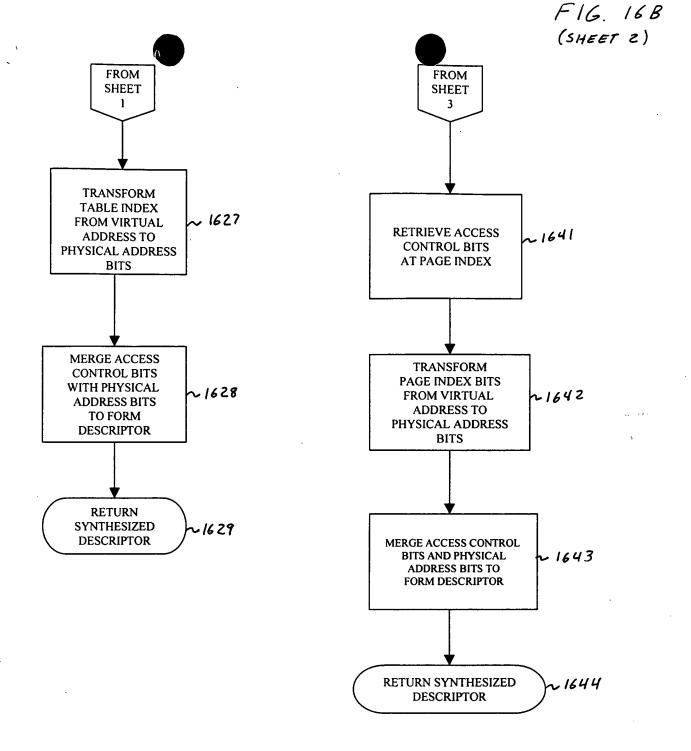


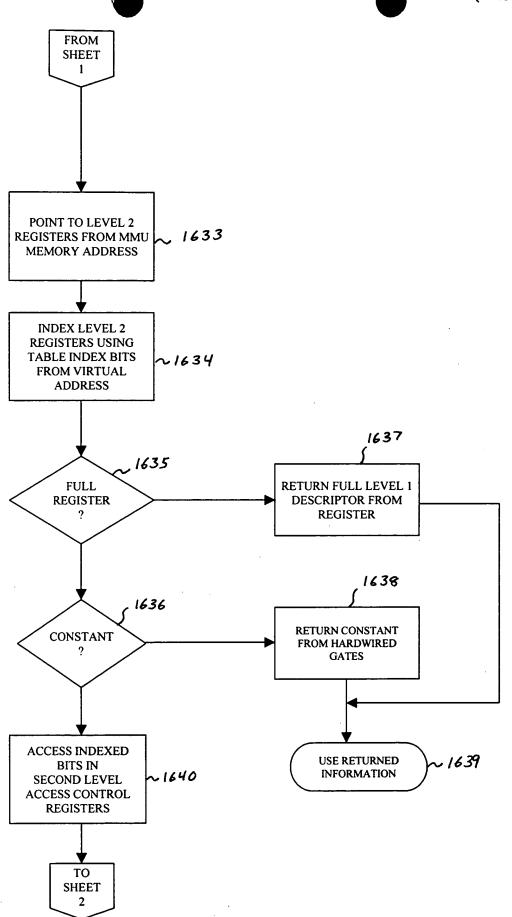
CONTINUE CACHE LOCK
PROCEDURE

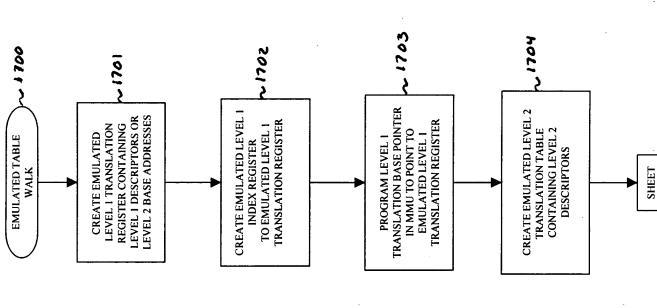












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